

# LM8262

*LM8262 Dual RRIO, High Output Current & Unlimited Cap Load Op Amp in MSOP*



Literature Number: SNOS975D

# LM8262 Dual RRIO, High Output Current & Unlimited Cap Load Op Amp in MSOP

## General Description

The LM8262 is a Rail-to-Rail input and output Op Amp which can operate with a wide supply voltage range. This device has high output current drive, greater than Rail-to-Rail input common mode voltage range, unlimited capacitive load drive capability, and provides tested and guaranteed high speed and slew rate. It is specifically designed to handle the requirements of flat panel TFT panel  $V_{COM}$  driver applications as well as being suitable for other low power, and medium speed applications which require ease of use and enhanced performance over existing devices.

Greater than Rail-to-Rail input common mode voltage range with 50dB of Common Mode Rejection, allows high side and low side sensing, among many applications, without having any concerns over exceeding the range and no compromise in accuracy. In addition, most device parameters are insensitive to power supply variations; this design enhancement is yet another step in simplifying its usage. The output stage has low distortion (0.05% THD+N) and can supply a respectable amount of current (15mA) with minimal headroom from either rail (300mV).

The LM8262 is offered in the space saving MSOP package.

## Features

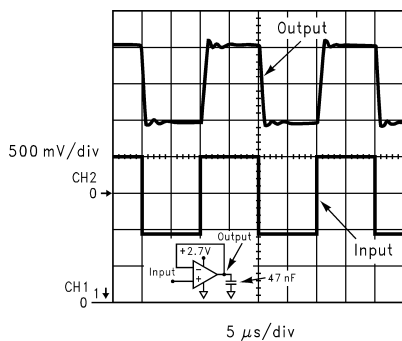
( $V_S = 5V$ ,  $T_A = 25^\circ C$ , Typical values unless specified).

- GBWP 21MHz
- Wide supply voltage range 2.5V to 22V
- Slew rate 12V/ $\mu s$
- Supply current/channel 1.15 mA
- Cap load limit Unlimited
- Output short circuit current +53mA/-75mA
- +/-5% Settling time 400ns (500pF, 100mV<sub>PP</sub> step)
- Input common mode voltage 0.3V beyond rails
- Input voltage noise 15nV/ $\sqrt{Hz}$
- Input current noise 1pA/ $\sqrt{Hz}$
- THD+N < 0.05%

## Applications

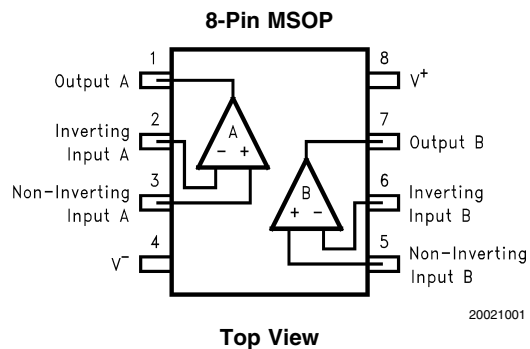
- TFT-LCD flat panel  $V_{COM}$  driver
- A/D converter buffer
- High side/low side sensing
- Headphone amplifier

Output Response with Heavy Capacitive Load



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Connection Diagram



20021001

## Ordering Information

Package	Part Number	Package Marking	Media Transport	NSC Drawing
8-Pin MSOP	LM8262MM	A46	1k Units Tape and Reel	MUA08A
	LM8262MMX		3.5k Units Tape and Reel	

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**Absolute Maximum Ratings** (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

ESD Tolerance	2KV (Note 2) 200V (Note 9)
V <sub>IN</sub> Differential	+/-10V
Output Short Circuit Duration	(Notes 3, 11)
Supply Voltage (V <sup>+</sup> - V <sup>-</sup> )	24V
Voltage at Input/Output pins	V <sup>+</sup> +0.8V, V <sup>-</sup> -0.8V
Storage Temperature Range	-65°C to +150°C
Junction Temperature (Note 4)	+150°C

Soldering Information:

Infrared or Convection (20 sec.)	235°C
Wave Soldering (10 sec.)	260°C

**Operating Ratings**

Supply Voltage (V <sup>+</sup> - V <sup>-</sup> )	2.5V to 22V
Junction Temperature Range (Note 4)	-40°C to +85°C
Package Thermal Resistance, $\theta_{JA}$ , (Note 4)	8-Pin MSOP 235°C/W

**2.7V Electrical Characteristics**

Unless otherwise specified, all limits guaranteed for T<sub>J</sub> = 25°C, V<sup>+</sup> = 2.7V, V<sup>-</sup> = 0V, V<sub>CM</sub> = 0.5V, V<sub>O</sub> = V<sup>+</sup>/2, and R<sub>L</sub> > 1M $\Omega$  to V<sup>-</sup>. **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Condition	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
V <sub>OS</sub>	Input Offset Voltage	V <sub>CM</sub> = 0.5V & V <sub>CM</sub> = 2.2V	-	+/-0.7	+/-5 <b>+/-7</b>	mV
TC V <sub>OS</sub>	Input Offset Average Drift	V <sub>CM</sub> = 0.5V & V <sub>CM</sub> = 2.2V (Note 12)	-	+/-2	-	$\mu$ V/C
I <sub>B</sub>	Input Bias Current	V <sub>CM</sub> = 0.5V (Note 7)	-	-1.20	-2.00 <b>-2.70</b>	$\mu$ A
		V <sub>CM</sub> = 2.2V (Note 7)	-	+0.49	+1.00 <b>+1.60</b>	
I <sub>OS</sub>	Input Offset Current	V <sub>CM</sub> = 0.5V & V <sub>CM</sub> = 2.2V	-	20	250 <b>400</b>	nA
CMRR	Common Mode Rejection Ratio	V <sub>CM</sub> stepped from 0V to 1.0V	76 <b>60</b>	100	-	dB
		V <sub>CM</sub> stepped from 1.7V to 2.7V	-	100	-	
		V <sub>CM</sub> stepped from 0V to 2.7V	58 <b>50</b>	70	-	
+PSRR	Positive Power Supply Rejection Ratio	V <sup>+</sup> = 2.7V to 5V	78 <b>74</b>	104	-	dB
CMVR	Input Common-Mode Voltage Range	CMRR > 50dB	-	-0.3	-0.1 <b>0.0</b>	V
			2.8 <b>2.7</b>	3.0	-	V
A <sub>VOL</sub>	Large Signal Voltage Gain	V <sub>O</sub> = 0.5 to 2.2V, R <sub>L</sub> = 10k to V <sup>-</sup>	70 <b>67</b>	78	-	dB
		V <sub>O</sub> = 0.5 to 2.2V, R <sub>L</sub> = 2k to V <sup>-</sup>	67 <b>63</b>	73	-	dB
V <sub>O</sub>	Output Swing High	R <sub>L</sub> = 10k to V <sup>-</sup>	2.49 <b>2.46</b>	2.59	-	V
		R <sub>L</sub> = 2k to V <sup>-</sup>	2.45 <b>2.41</b>	2.53	-	
	Output Swing Low	R <sub>L</sub> = 10k to V <sup>-</sup>	-	90	100 <b>120</b>	mV
I <sub>SC</sub>	Output Short Circuit Current	Sourcing to V <sup>-</sup> V <sub>ID</sub> = 200mV (Note 10)	30 <b>20</b>	48	-	mA
		Sinking to V <sup>+</sup> V <sub>ID</sub> = -200mV (Note 10)	50 <b>30</b>	65	-	

## 2.7V Electrical Characteristics (Continued)

Unless otherwise specified, all limits guaranteed for  $T_J = 25^\circ\text{C}$ ,  $V^+ = 2.7\text{V}$ ,  $V^- = 0\text{V}$ ,  $V_{\text{CM}} = 0.5\text{V}$ ,  $V_O = V^+/2$ , and  $R_L > 1\text{M}\Omega$  to  $V^-$ . **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Condition	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
$I_S$	Supply Current (both amps)	No load, $V_{\text{CM}} = 0.5\text{V}$	–	2.0	2.5 <b>3.0</b>	mA
SR	Slew Rate (Note 8)	$A_V = +1, V_I = 2V_{\text{PP}}$	–	9	–	V/ $\mu\text{s}$
$f_u$	Unity Gain-Frequency	$V_I = 10\text{mV}$ , $R_L = 2\text{k}\Omega$ to $V^+/2$	–	10	–	MHz
GBWP	Gain Bandwidth Product	$f = 50\text{KHz}$	15.5 <b>14</b>	21	–	MHz
$\Phi_{\text{m}}$	Phase Margin	$V_I = 10\text{mV}$	–	50	–	Deg
$e_n$	Input-Referred Voltage Noise	$f = 2\text{KHz}$ , $R_S = 50\Omega$	–	15	–	nV/ $\sqrt{\text{Hz}}$
$i_n$	Input-Referred Current Noise	$f = 2\text{KHz}$	–	1	–	pA/ $\sqrt{\text{Hz}}$
$f_{\text{max}}$	Full Power Bandwidth	$Z_L = (20\text{pF} \parallel 10\text{k}\Omega)$ to $V^+/2$	–	1	–	MHz

## 5V Electrical Characteristics

Unless otherwise specified, all limited guaranteed for  $T_J = 25^\circ\text{C}$ ,  $V^+ = 5\text{V}$ ,  $V^- = 0\text{V}$ ,  $V_{\text{CM}} = 1\text{V}$ ,  $V_O = V^+/2$ , and  $R_L > 1\text{M}\Omega$  to  $V^-$ . **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Condition	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
$V_{\text{OS}}$	Input Offset Voltage	$V_{\text{CM}} = 1\text{V}$ & $V_{\text{CM}} = 4.5\text{V}$	–	+/-0.7	+/-5 <b>+/- 7</b>	mV
TC $V_{\text{OS}}$	Input Offset Average Drift	$V_{\text{CM}} = 1\text{V}$ & $V_{\text{CM}} = 4.5\text{V}$ (Note 12)	–	+/-2	–	$\mu\text{V}/^\circ\text{C}$
$I_B$	Input Bias Current	$V_{\text{CM}} = 1\text{V}$ (Note 7)	–	-1.18	-2.00 <b>-2.70</b>	$\mu\text{A}$
		$V_{\text{CM}} = 4.5\text{V}$ (Note 7)	–	+0.49	+1.00 <b>+1.60</b>	
$I_{\text{OS}}$	Input Offset Current	$V_{\text{CM}} = 1\text{V}$ & $V_{\text{CM}} = 4.5\text{V}$	–	20	250 <b>400</b>	nA
CMRR	Common Mode Rejection Ratio	$V_{\text{CM}}$ stepped from 0V to 3.3V	84 <b>72</b>	110	–	dB
		$V_{\text{CM}}$ stepped from 4V to 5V	–	100	–	
		$V_{\text{CM}}$ stepped from 0V to 5V	64 <b>61</b>	80	–	
+PSRR	Positive Power Supply Rejection Ratio	$V^+ = 2.7\text{V}$ to 5V, $V_{\text{CM}} = 0.5\text{V}$	78 <b>74</b>	104	–	dB
CMVR	Input Common-Mode Voltage Range	CMRR > 50dB	–	-0.3	-0.1 <b>0.0</b>	V
			5.1 <b>5.0</b>	5.3	–	V
$A_{\text{VOL}}$	Large Signal Voltage Gain	$V_O = 0.5$ to 4.5V, $R_L = 10\text{k}$ to $V^-$	74 <b>70</b>	84	–	dB
		$V_O = 0.5$ to 4.5V, $R_L = 2\text{k}$ to $V^-$	70 <b>66</b>	80	–	
$V_O$	Output Swing High	$R_L = 10\text{k}$ to $V^-$	4.75 <b>4.72</b>	4.87	–	V
		$R_L = 2\text{k}$ to $V^-$	4.70 <b>4.66</b>	4.81	–	
	Output Swing Low	$R_L = 10\text{k}$ to $V^-$	–	86	125 <b>135</b>	mV

## 5V Electrical Characteristics (Continued)

Unless otherwise specified, all limited guaranteed for  $T_J = 25^\circ\text{C}$ ,  $V^+ = 5\text{V}$ ,  $V^- = 0\text{V}$ ,  $V_{\text{CM}} = 1\text{V}$ ,  $V_O = V^+/2$ , and  $R_L > 1\text{M}\Omega$  to  $V^-$ . **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Condition	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
$I_{\text{SC}}$	Output Short Circuit Current	Sourcing to $V^-$ $V_{\text{ID}} = 200\text{mV}$ (Note 10)	35 <b>20</b>	53	–	mA
		Sinking to $V^+$ $V_{\text{ID}} = -200\text{mV}$ (Note 10)	60 <b>50</b>	75	–	
$I_{\text{S}}$	Supply Current (both amps)	No load, $V_{\text{CM}} = 1\text{V}$	–	2.3	2.8 <b>3.5</b>	mA
SR	Slew Rate (Note 8)	$A_V = +1$ , $V_I = 5V_{\text{PP}}$	10 <b>7</b>	12	–	V/ $\mu\text{s}$
$f_u$	Unity Gain Frequency	$V_I = 10\text{mV}$ , $R_L = 2\text{k}\Omega$ to $V^+/2$	–	10.5	–	MHz
GBWP	Gain-Bandwidth Product	$f = 50\text{KHz}$	16 <b>15</b>	21	–	MHz
$\text{Phi}_m$	Phase Margin	$V_I = 10\text{mV}$	–	53	–	Deg
$e_n$	Input-Referred Voltage Noise	$f = 2\text{KHz}$ , $R_S = 50\Omega$	–	15	–	nV/ $\sqrt{\text{Hz}}$
$i_n$	Input-Referred Current Noise	$f = 2\text{KHz}$	–	1	–	pA/ $\sqrt{\text{Hz}}$
$f_{\text{max}}$	Full Power Bandwidth	$Z_L = (20\text{pF} \parallel 10\text{k}\Omega)$ to $V^+/2$	–	900	–	KHz
$t_s$	Settling Time (+/-5%)	100mV <sub>PP</sub> Step, 500pF load	–	400	–	ns
THD+N	Total Harmonic Distortion + Noise	$R_L = 1\text{k}\Omega$ to $V^+/2$ $f = 10\text{KHz}$ to $A_V = +2$ , 4V <sub>PP</sub> swing	–	0.05	–	%

## +/-11V Electrical Characteristics

Unless otherwise specified, all limited guaranteed for  $T_J = 25^\circ\text{C}$ ,  $V^+ = 11\text{V}$ ,  $V^- = -11\text{V}$ ,  $V_{\text{CM}} = 0\text{V}$ ,  $V_O = 0\text{V}$ , and  $R_L > 1\text{M}\Omega$  to  $0\text{V}$ . **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Condition	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
$V_{\text{OS}}$	Input Offset Voltage	$V_{\text{CM}} = -10.5\text{V}$ & $V_{\text{CM}} = 10.5\text{V}$	–	+/-0.7	+/-7 <b>+/- 9</b>	mV
TC $V_{\text{OS}}$	Input Offset Average Drift	$V_{\text{CM}} = -10.5\text{V}$ & $V_{\text{CM}} = 10.5\text{V}$ (Note 12)	–	+/-2	–	$\mu\text{V}/^\circ\text{C}$
$I_{\text{B}}$	Input Bias Current	$V_{\text{CM}} = -10.5\text{V}$ (Note 7)	–	-1.05	-2.00 <b>-2.80</b>	$\mu\text{A}$
		$V_{\text{CM}} = 10.5\text{V}$ (Note 7)	–	+0.49	+1.00 <b>+1.50</b>	
$I_{\text{OS}}$	Input Offset Current	$V_{\text{CM}} = -10.5\text{V}$ & $V_{\text{CM}} = 10.5\text{V}$	–	30	275 <b>550</b>	nA
CMRR	Common Mode Rejection Ratio	$V_{\text{CM}}$ stepped from -11V to 9V	84 <b>80</b>	100	–	dB
		$V_{\text{CM}}$ stepped from 10V to 11V	–	100	–	
		$V_{\text{CM}}$ stepped from -11V to 11V	74 <b>72</b>	88	–	
+PSRR	Positive Power Supply Rejection Ratio	$V^+ = 9\text{V}$ to 11V	70 <b>66</b>	100	–	dB
-PSRR	Negative Power Supply Rejection Ratio	$V^- = -9\text{V}$ to -11V	70 <b>66</b>	100	–	dB

**+/-11V Electrical Characteristics** (Continued)

Unless otherwise specified, all limited guaranteed for  $T_J = 25^\circ\text{C}$ ,  $V^+ = 11\text{V}$ ,  $V^- = -11\text{V}$ ,  $V_{\text{CM}} = 0\text{V}$ ,  $V_O = 0\text{V}$ , and  $R_L > 1\text{M}\Omega$  to  $0\text{V}$ . **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Condition	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
CMVR	Input Common-Mode Voltage Range	CMRR > 50dB	-	-11.3	-11.1 <b>-11.0</b>	V
			11.1 <b>11.0</b>	11.3	-	V
A <sub>VOL</sub>	Large Signal Voltage Gain	V <sub>O</sub> = 0V to +/-9V, R <sub>L</sub> = 10k $\Omega$	78 <b>74</b>	85	-	dB
		V <sub>O</sub> = 0V to +/-9V, R <sub>L</sub> = 2k $\Omega$	72 <b>66</b>	79	-	
V <sub>O</sub>	Output Swing High	R <sub>L</sub> = 10k $\Omega$	10.65 <b>10.61</b>	10.77	-	V
		R <sub>L</sub> = 2k $\Omega$	10.6 <b>10.55</b>	10.69	-	
	Output Swing Low	R <sub>L</sub> = 10k $\Omega$	-	-10.98	-10.75 <b>-10.65</b>	V
		R <sub>L</sub> = 2k $\Omega$	-	-10.91	-10.65 <b>-10.6</b>	
I <sub>SC</sub>	Output Short Circuit Current	Sourcing to ground V <sub>ID</sub> = 200mV (Note 10)	40 <b>25</b>	60	-	mA
		Sinking to ground V <sub>ID</sub> = 200mV (Note 10)	65 <b>55</b>	100	-	
I <sub>S</sub>	Supply Current	No load, V <sub>CM</sub> = 0V	-	2.5	4 <b>5</b>	mA
SR	Slew Rate (Note 8)	A <sub>V</sub> = +1, V <sub>I</sub> = 16V <sub>PP</sub>	10 <b>8</b>	15	-	V/ $\mu\text{s}$
f <sub>U</sub>	Unity Gain Frequency	V <sub>I</sub> = 10mV, R <sub>L</sub> = 2k $\Omega$	-	13	-	MHz
GBWP	Gain-Bandwidth Product	f = 50KHz	18 <b>16</b>	24	-	MHz
Phi <sub>m</sub>	Phase Margin	V <sub>I</sub> = 10mV	-	58	-	Deg
e <sub>n</sub>	Input-Referred Voltage Noise	f = 2KHz, R <sub>S</sub> = 50 $\Omega$	-	15	-	nV/ $\sqrt{\text{Hz}}$
i <sub>n</sub>	Input-Referred Current Noise	f = 2KHz	-	1	-	pA/ $\sqrt{\text{Hz}}$
t <sub>S</sub>	Settling Time (+/-1%, A <sub>V</sub> = +1)	Positive Step, 5V <sub>PP</sub>	-	320	-	ns
		Negative Step, 5V <sub>PP</sub>	-	600	-	
THD+N	Total Harmonic Distortion +Noise	R <sub>L</sub> = 1k $\Omega$ , f = 10KHz, A <sub>V</sub> = +2, 15V <sub>PP</sub> swing	-	0.01	-	%
CT <sub>REJ</sub>	Cross-Talk Rejection	f = 5MHz, Driver R <sub>L</sub> = 10k $\Omega$	-	68	-	dB

## +/-11V Electrical Characteristics (Continued)

**Note 1:** Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Rating indicate conditions for which the device is intended to be functional, but specific performance is not guaranteed. For guaranteed specifications and the test conditions, see the Electrical Characteristics.

**Note 2:** Human body model, 1.5k $\Omega$  in series with 100pF.

**Note 3:** Applies to both single-supply and split-supply operation. Continuous short circuit operation at elevated ambient temperature can result in exceeding the maximum allowed junction temperature of 150°C.

**Note 4:** The maximum power dissipation is a function of  $T_J(\text{max})$ ,  $\theta_{JA}$ , and  $T_A$ . The maximum allowable power dissipation at any ambient temperature is  $P_D = (T_J(\text{max}) - T_A) / \theta_{JA}$ . All numbers apply for packages soldered directly onto a PC board.

**Note 5:** Typical Values represent the most likely parametric norm.

**Note 6:** All limits are guaranteed by testing or statistical analysis.

**Note 7:** Positive current corresponds to current flowing into the device.

**Note 8:** Slew rate is the slower of the rising and falling slew rates. Connected as a Voltage Follower.

**Note 9:** Machine Model, 0 $\Omega$  in series with 200pF.

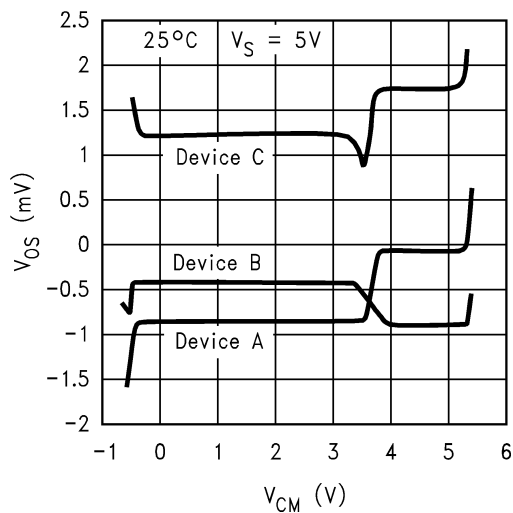
**Note 10:** Short circuit test is a momentary test. See Note 11.

**Note 11:** Output short circuit duration is infinite for  $V_S \leq 6V$  at room temperature and below. For  $V_S > 6V$ , allowable short circuit duration is 1.5ms.

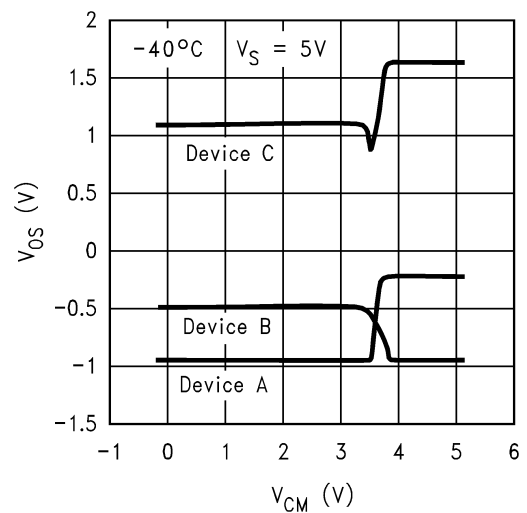
**Note 12:** Offset voltage average drift determined by dividing the change in  $V_{OS}$  at temperature extremes into the total temperature change.

## Typical Performance Characteristics $T_A = 25^\circ\text{C}$ , Unless Otherwise Noted

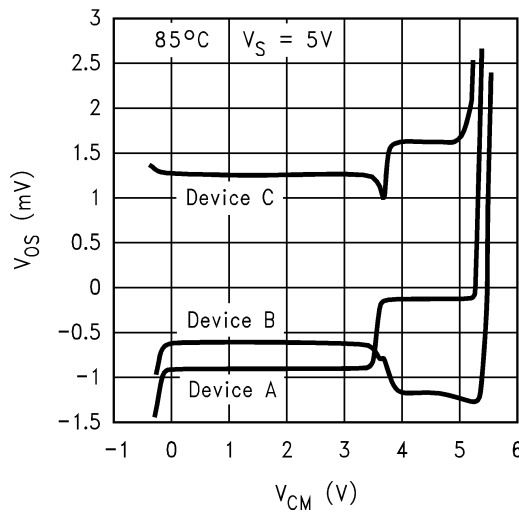
$V_{OS}$  vs.  $V_{CM}$  for 3 Representative Units



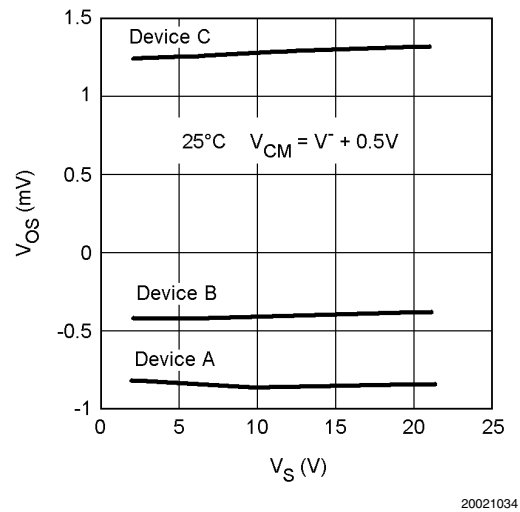
$V_{OS}$  vs.  $V_{CM}$  for 3 Representative Units



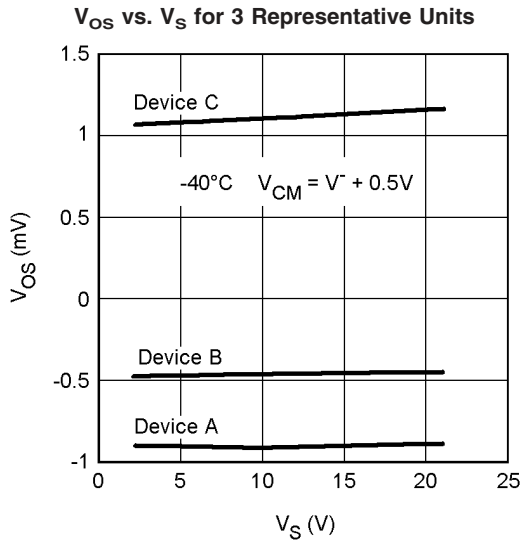
$V_{OS}$  vs.  $V_{CM}$  for 3 Representative Units



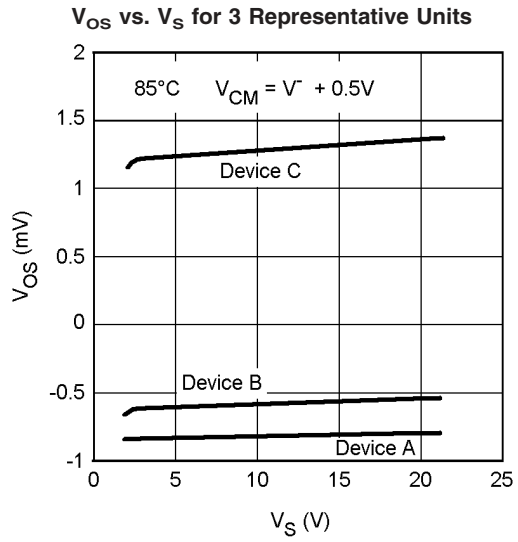
$V_{OS}$  vs.  $V_S$  for 3 Representative Units



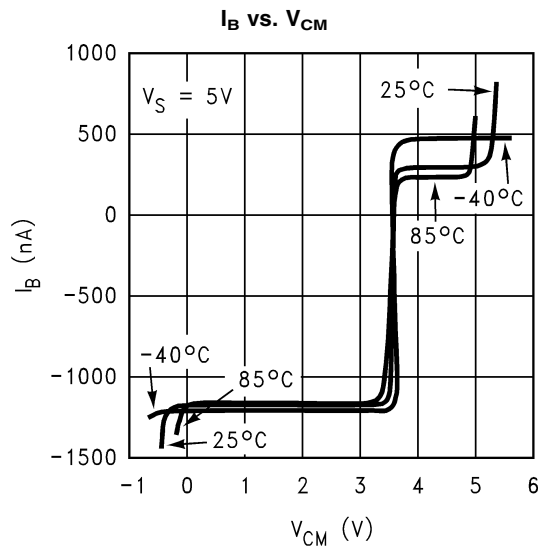
**Typical Performance Characteristics**  $T_A = 25^\circ\text{C}$ , Unless Otherwise Noted (Continued)



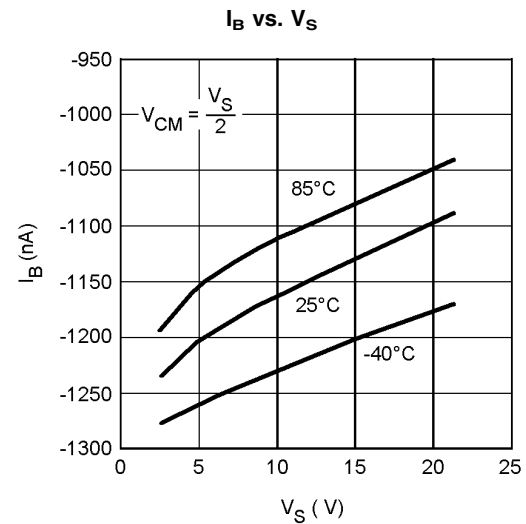
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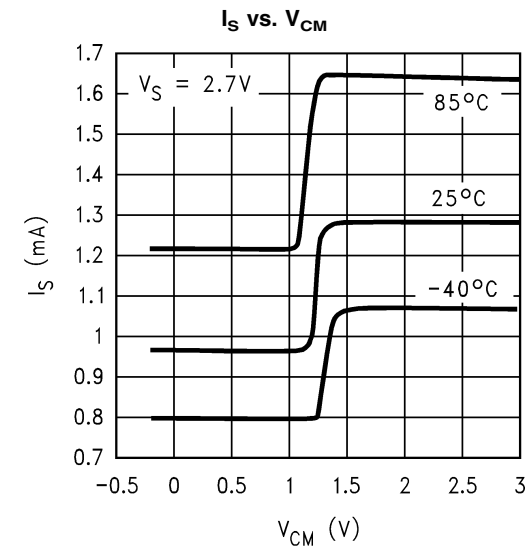
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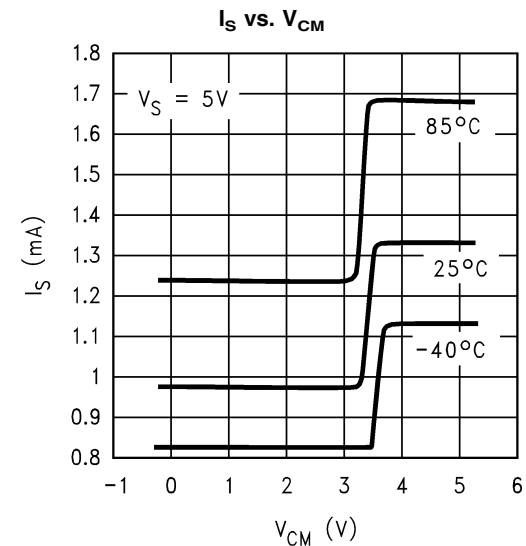
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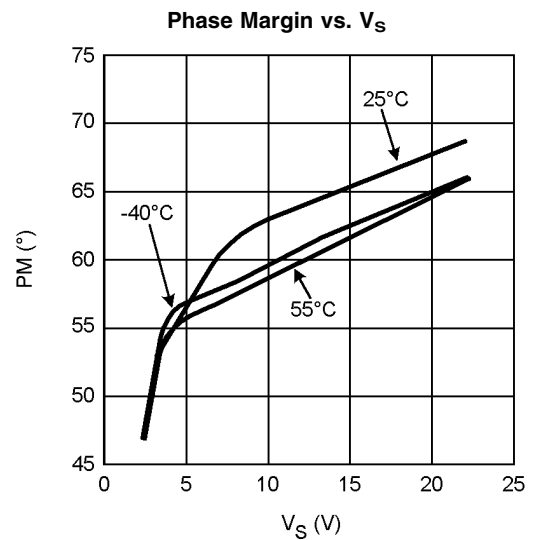
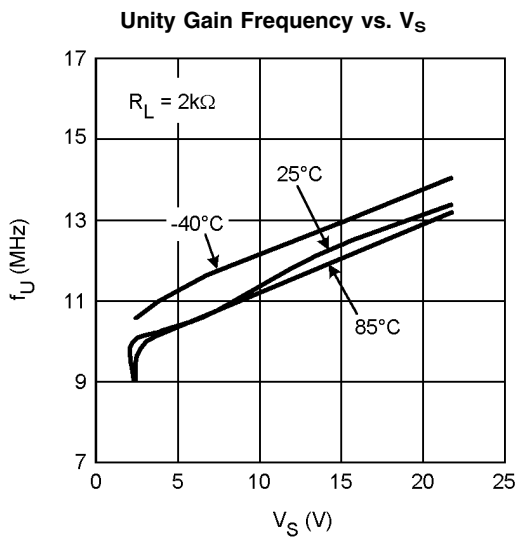
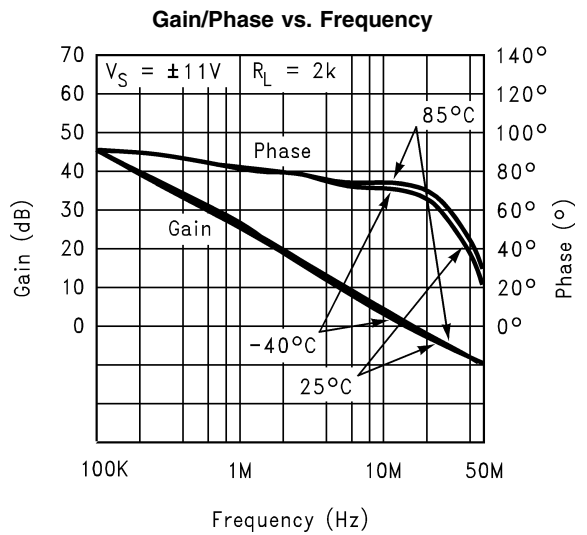
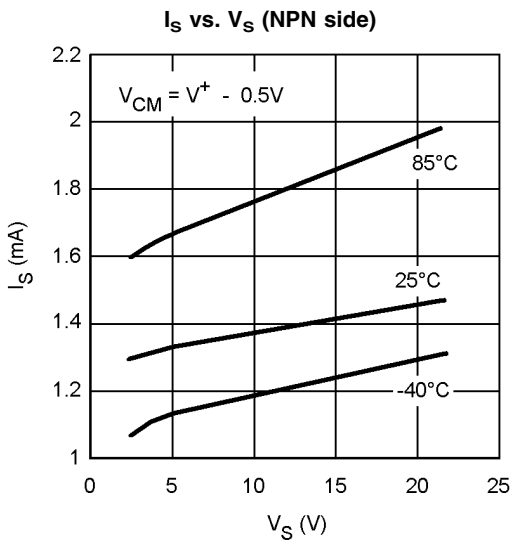
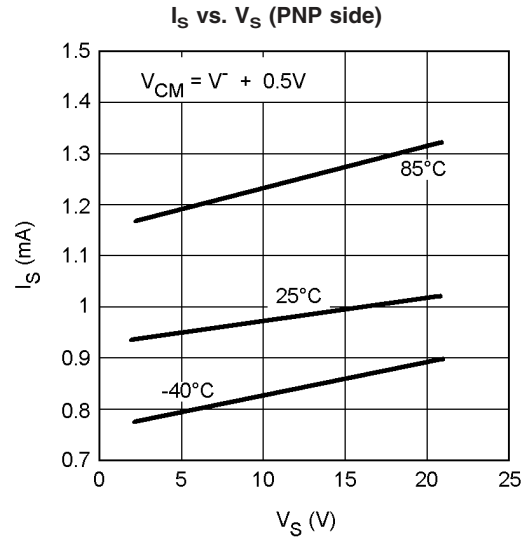
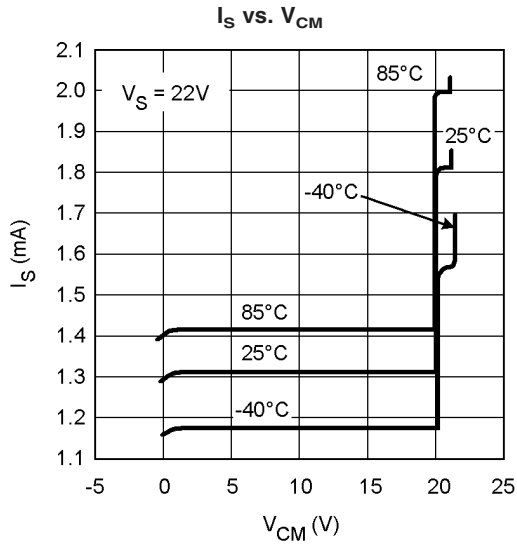


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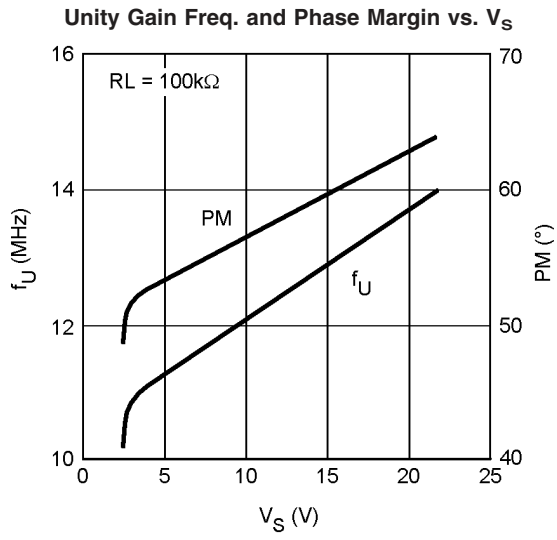


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Typical Performance Characteristics  $T_A = 25^\circ\text{C}$ , Unless Otherwise Noted (Continued)



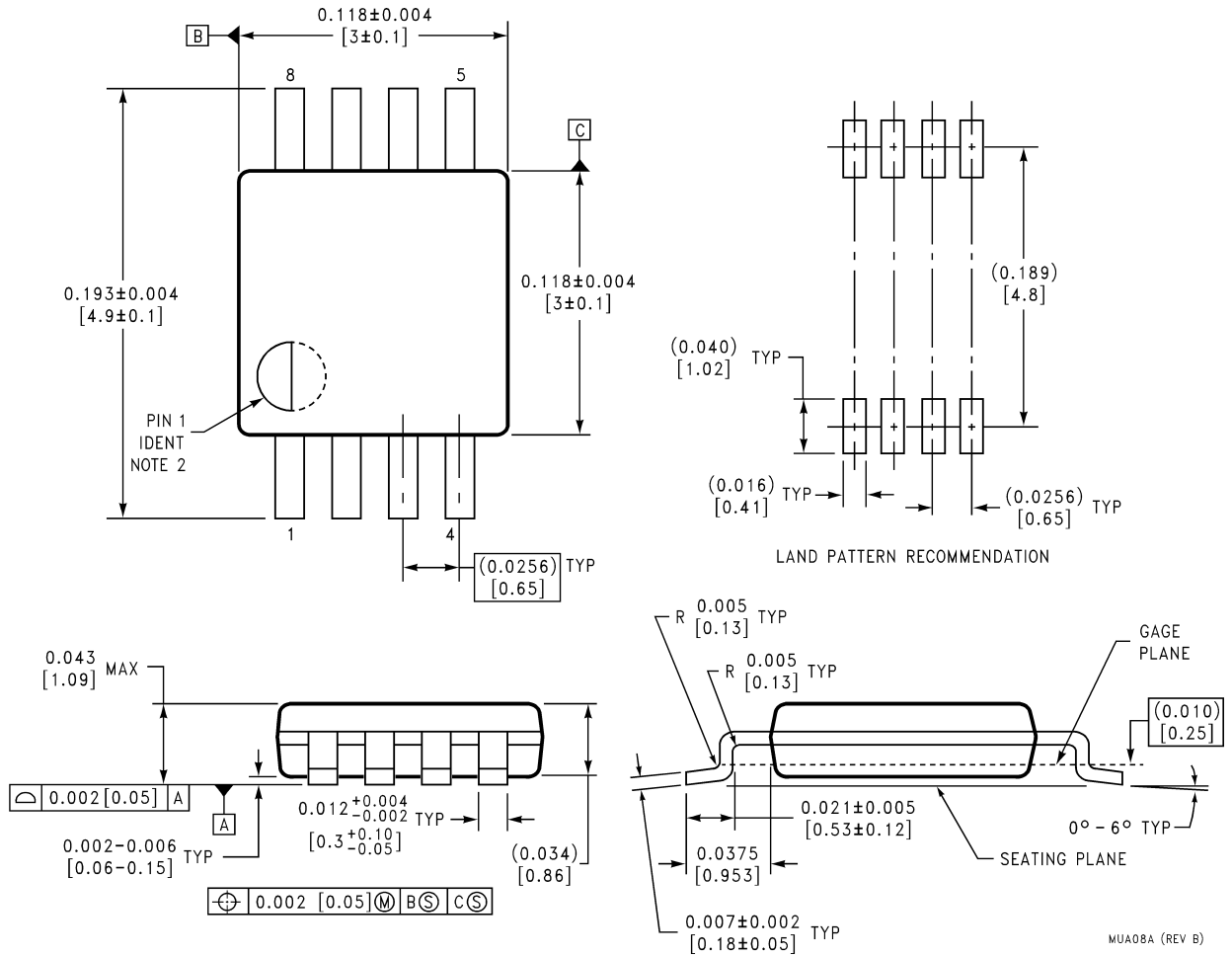
# Typical Performance Characteristics $T_A = 25^\circ\text{C}$ , Unless Otherwise Noted (Continued)



20021004

## Physical Dimensions inches (millimeters)

unless otherwise noted



**8-Pin MSOP**  
**NS Package Number MUA08A**

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